



P1394 to ATA Bridge

Overview

The inic-1430 provides an advanced solution to connect ATAPI or EIDE devices to IEEE-1394a interface with integrated CPU and embedded SRAM. To provide high performance and cost effective solution, the inic-1430 integrates 1394a link core, ATA control block and microprocessor into a single ASIC. The inic-1430 provides the data transfer rate of up to 50 MB/sec, its ATA interface supports ultra DMA modes (33/66/100 MB/sec).

Feature Summary

- IEEE Std 1394-1995 and 1394a Compliant
- Support industry standard PHY
- Support Asynchronous Transfers at 100, 200 and 400 Mb/s
- Perform 1394a Cycle Master
- Implement SBP-2 stack to optimize the performance
- T13 1410D ATA/ATAPI-6 Compliant

- Support DMA mode 0-2, and UDMA mode 2, 4 and 5 (up to 100MB/s)
- Integrated internal CPU with embedded SRAM
- Implement the firmware download mechanism
- Local Bus Interface to serial NVRAM and Flash memory
- Up to 17 GPIO pins
- Low power CMOS with 3.3Volts
- 128-pin LQFP package

Firmware/Software Support

- Protocols support including SBP-2, MMC-2, RBC and ATA/ATAPI
- Provide software utilities for downloading the upgraded firmware code

Devices Support

- Hard disk drives
- CD-RW devices
- DVDs
- Removable media devices

Block Diagram

